

## CLAIMS

1. A method for testing a plurality of integrated circuit chips for compliance with a radio standard, each chip having transmission means and receiving means for sending and receiving RF signals, the method comprising:

- placing the chips in close proximity to one another,
- testing (100) the transmission means of each chip with respect to a known good reference chip,
- selecting (104) a number of the chips to form a generating group, the remaining chips forming a receiving group, and
- testing the receiving group (106) using signals generated by the generating group.

2. A method according to claim 1, wherein the chips are mounted on a test board prior to testing.

3. A method according to claim 1 or claim 2, wherein the selection of generating and receiving groups (104, 106) is repeated (108, 110) until all chips have been tested.

4. A method according to claim 3, wherein the number of chips selected to form a generating group is determined at least in part by the testing requirements.

5. A method according to claim 4, wherein the testing requirements specify a test requiring at least two generated signals.

6. A method according to claim 4 or claim 5, wherein the testing requirements specify an intermodulation test requiring three generated signals.

7. A testing system for testing a plurality of integrated circuit chips, each chip having transmission means (10, 20) and receiving means (10, 30) for sending and receiving RF signals, the system comprising:

5 a computer (40) having communication, control and data acquisition means (55) for communicating with, controlling and acquiring data from testing means to which it is connected,

the testing means (80) comprising a plurality of chip sockets (82a, b, c) adapted to physically accept and electronically interface with a chip placed therein, and wherein each socket is provided with signal propagation (88) and  
10 attenuation means (86) for sending and receiving signals to each of the other sockets under the control of the computer (40),

and where in operation the computer selects a group of chips (82b) to generate test signals which are propagated via the propagation means to a reception test group of chips (82a).

15

8. A system according to claim 7, wherein the sockets are located on a test board 80.

9. A system according to claim 7 or claim 8, wherein the signals  
20 generated by the selected group are radio frequency signals.

10. A test board (80) comprising a plurality of chip sockets (82a, b, c) adapted to physically accept and electronically interface with a chip placed therein, and wherein each socket is provided with signal propagation (88) and  
25 attenuation means (86) for sending and receiving test signals generated by at least one chip to each of the other sockets.

11. A computer program comprising instructions for performing a method according to any of claims 1 to 4 when run on a testing computer (40).

30

12. A computer readable storage medium (60) having recorded thereon data representing instructions for performing a method according to any of claims 1 to 6 when said data is loaded on a testing computer (40).